Name of Teaching Staff	:	Prof. Mrunalini Pimpale	
Designation	:	Assistant Professor	
Department	:	Electronics & Telecommunication Engineering	
Date of Joining the Institution	:	04.7.2013	
Email ID	:	Mrunalini.ingle@djsce.ac.in	
Office Contact	:	022-42331214	
Google Scholar Link		https://scholar.google.com/citations?hl=en&user=CUvPuEE AAAAJ	
ORCID	:	https://orcid.org/my-orcid?orcid=0000-0002-9099-4680	
Research gate Link:	:	https://www.researchgate.net/profile/Mrunalini-Pimpale	
Publons Researcher ID	:	https://publons.com/researcher/4917063/mrunalini-pimpale/	
Qualifications with Class / Grade	:	 M.Tech. – Electronics Engineering from Yashwantrao Chavan College of Engineering, Nagpur Autonomous in June, 2012, 1st class with CGPA 7.59 B.E. (Electronics Engineering) from Rashtrasant Tukadoji Maharaj Nagpur University in June 2008, 1st class with 71.6%. 	
Total Experience in Years	:	 Teaching: 12 years Assistant Professor, D.J. Sanghvi College of Engineering (Electronics and Telecommunication Engineering Department) from 4.7.2013 to till date. Assistant Professor, D.J. Sanghvi College of Engineering (Engineering Department) from 11.7.2012 to 30.6.2013 on Adhoc basis. Lecturer, Yashwantrao Chavan College of Engineering Nagpur, from 11.08.2009 to 30.04.2012. 	
Papers Presented in Conferences	:	 International: 3 [1] Rohan Vaidya, Darshan Trivedi, Sagar Satra, Mrunalini Pimpale "A Novel Approach Towards Handwritten Character Recognition Using Deep Learning" Proceeding of 2018 Second International Conference on Inventive Communication and Computational Technologies (ICICCT), IEEE, 2018, pp 772-775. April 2018 [2] Mrunalini Pimpale, Tejaswini Panse, "Radix-10 Parallel Decimal Multiplier" Proceedings of International Conference on Electronics and Communication Engineering, January 2012 [3] Mrunalini Pimpale, Tejaswini Panse," Fixed –Point Parallel Decimal Multiplier" ICEECS-2012, May 2012 	
Area of Specialization		Analog VLSI, Analog Signal Processing, Embedded Systems.	
Professional Memberships	:	Life Member of Indian Society of Technical Education (ISTE)	

Subjects Taught	L	JG Level:
		[1] Digital Communication
		[2] Analog Electronics –I
		[3] Analog Electronics –II
		[4] Analog circuit Design
		[5] Integrated Circuits
		[6] Electronics Devices and circuits-I
		[7] Electronics Devices and circuits-II
		[8] VLSI Design
		[9] Microprocessor and Microcontroller
		[10] Digital System Design
		[11] Electronics and Measurement Instrumentation
Projects Guided	: <u>U</u>	J <u>G Level:</u> (Last 4 years) [1] Object Detecting Autonomous Water Surface Cleaning Bot.
		[2] Sketch recognition using Siamese network.
		[3] Smart Visual Local Navigation AID for the Visually Impaired.
		[4] Metal Detector Robotic Vehicle.
		[5] Closed Loop BLDC motor for Electric cars.
		[6] Wireless Rover with Robotic Arm and Visual Assistance.
		[7] Sleep detector using EEG signal.
		[8] Gesture and Voice control Mouse.
		[9] SEO optimizer and suggester.
		[10] Handwriting detection and recognition using Neural Network.
		[11] Smart trolley using RFID.
		[12] Analysis of 5G network.

Recommended Students	Name of the Student	University/Industry			
for Higher Education	Aniket Rao	• University of Michigan Rackham			
		Graduate School			
	Shraddha Sutar	• University of Illinois			
	Ayush Shah	Georgia Tech Graduate Admissions			
	Vidhi Raval	• University of Colorado Boulder			
	Sagar Samant	• Texas University			
	Nikit Shah	• Clemson University (Graduate)			
	Chirag Shah	• University of Pennsylvania			
	Vanthana Sachdev	• University of Pennsylvania			
	Mitali Salvi	Georgia Tech Graduate Admissions			
	Priyal Raut	• University of Pennsylvania			
	Raj Visharia	Georgia Tech Graduate Admissions			
	Rohan Deo	• University of Sadan California			
	Harsh Raval	Oxford University			
	Prakhar Sah	• Virginia Polytechnic Institute & State University			
	Riddhi Thakkar	• Worcester Polytechnic Institute			
Institute/Department	➢ NAAC Criteria 5				
Responsibility handled:	NBA Criteria 8				
	 Electronics Design/ EHW & simulation Lab In-charge 				
	 Admission Committee Member 				
	➢ SE Class Teacher.				
	 Academic Bulletin (half yearly news editor) 				
	Department Website Coordinator.				
	Part of organizing committee of International conference (ICWiCOM-				
	2021).				
	Organized STTP on "Rece	nt Trends in wireless networks"			
	Organized Webinar on 'Re	ecent Trend in Microwave and Internet of			
	Things'.				